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Serial No.: 10/820,237
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Docket No.: ZIL-519-1C

Amendments to the Claims:

This listing of claims replaces all prior versions and listings of claims in the application.

Listing of Claims

1. (Original) A circuit for reducing east-west geometry mismatch between the top and bottom of a raster display, the circuit comprising:

 a first signal generator operable to generate a first signal having a parabolic portion;

 a second signal generator operable to generate a second signal having a parabolic portion; and

 a signal combiner operable to generate a third signal by combining the first signal and the second signal so that the parabolic portion of the first signal is continuous with the parabolic portion of the second signal.

2. (Original) The circuit of Claim 1 further comprising a horizontal deflection coil operable to receive the third signal.

3. (Original) The circuit of Claim 1 wherein the first signal generator comprises:

 a sawtooth generator operable to generate a sawtooth signal;

 one or more multipliers operable to generate one or more higher-order signals from the sawtooth signal; and

 a signal combiner operable to combine one or more of the higher-order signals to generate the first signal.

4. (Original) The circuit of Claim 1 wherein the second signal generator comprises:

 a sawtooth generator operable to generate a sawtooth signal;

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one or more multipliers operable to generate one or more higher-order signals from the sawtooth signal; and

a signal combiner operable to combine one or more of the higher-order signals to generate the second signal.

5. (Original) The circuit of Claim 1 wherein the second signal generator includes a level shifter.

6. (Original) The circuit of Claim 1 wherein the second signal generator includes an inverter.

7. (Original) The circuit of Claim 1 wherein the second signal generator includes a gain controller.

8. (Original) The circuit of Claim 1 wherein the circuit is implemented on a single integrated circuit device.

9-24. (Canceled)

25. (New) A method, comprising:

generating a sawtooth signal, wherein the sawtooth signal has an amplitude;

generating a correction signal, wherein the correction signal has a vertical retrace time t_{VR} and a vertical active time t_{VA} ;

modulating the amplitude of the sawtooth signal using the correction signal to generate a deflection signal; and

amplifying the deflection signal to generate a deflection current signal, wherein the deflection current signal is not distorted when the correction signal transitions from vertical retrace time t_{VR} to the vertical active time t_{VA} .

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26. (New) The method of Claim 25, wherein the generating the correction signal is performed by combining a first correction signal component with a second correction signal component such that the correction signal has no discontinuities.

27. (New) The method of Claim 26, wherein the first correction signal component has a constant amplitude during the vertical active time t_{VA} .

28. (New) The method of Claim 26, wherein the second correction signal component has a constant amplitude during the vertical retrace time t_{VR} .

29. (New) The method of Claim 26, wherein the first correction signal component has an amplitude, and wherein the amplitude of the first correction signal component varies parabolically over a portion of the first correction signal component.

30. (New) The method of Claim 25, wherein the sawtooth signal is a horizontal sawtooth signal, and wherein the correction signal is a horizontal correction signal.

31. (New) The method of Claim 25, wherein the generating the correction signal comprises generating a higher-order signal from the sawtooth signal.

32. (New) A horizontal deflection generator, comprising:

a circuit that generates a horizontal sawtooth signal having an amplitude;
and

means for modulating the amplitude of the horizontal sawtooth signal using a horizontal correction signal to generate a horizontal deflection current signal, wherein the horizontal correction signal has a vertical active time t_{VA} and a vertical retrace time t_{VR} , and wherein the horizontal deflection current signal is not

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distorted after a transition from the vertical active time t_{VA} to the vertical retrace time t_{VR} .

33. (New) The horizontal deflection generator of Claim 32, wherein the horizontal correction signal is a continuous signal.

34. (New) The horizontal deflection generator of Claim 32, wherein the means comprises an amplifier, wherein the means generates a modulated horizontal sawtooth signal, and wherein the amplifier generates the horizontal deflection current signal by amplifying the modulated horizontal sawtooth signal.

35. (New) The horizontal deflection generator of Claim 34, wherein the amplifier has a limited frequency bandwidth.

36. (New) The horizontal deflection generator of Claim 32, wherein the horizontal deflection generator is part of a raster display system.